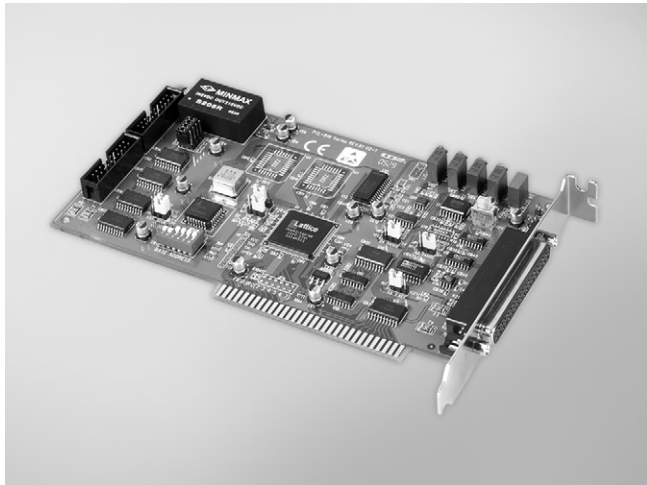


PCL-818HD/HG

PCL-818L

**100 kS/s, 12-bit, 16-ch ISA
Multifunction Card**

**40 kS/s, 12-bit, 16-ch ISA
Multifunction Card**



Features

- 16-ch single-ended or 8-ch differential analog input
- 12-bit A/D converter, with up to 100 kHz sampling rate
- Programmable gain
- Automatic channel/gain scanning
- Onboard FIFO memory (1,024 samples, PCL-818HD/HG only)
- One 12-bit analog output channel
- 16-ch digital input and 16-ch digital output
- Onboard programmable counter

Introduction

The PCL-818L series was designed for entry-level models to the PCL-818 series. The cards have been designed with the cost-sensitive customer in mind, but still offers the same functions as the rest of the series, except that they have a 40 kHz sampling rate and only accepts bipolar inputs. They are fully software and connector compatible with the PCL-818HD and PCL-818HG. This lets you upgrade your applications to these higher performance cards without hardware or software changes.

The PCL-818LS bundle consists of the PCL-818L card, the PCLD-8115 wiring terminal board and a DB37 cable assembly. The PCLD-8115 accommodates onboard passive signal conditioning components (resistors and capacitors), allowing you to easily implement a low-pass filter, a voltage attenuator or a 4 ~ 20 mA voltage converter.

Specifications

Analog Input

- **Channels** 16 single-ended / 8 differential
- **Resolution** 12 bits
- **Max. Sampling Rate** 100 kS/s for all input ranges (PCL-818HD/HG only)
40 kS/s for all input ranges (PCL-818L only)
- **FIFO Size** 1,024 samples
- **Overvoltage Protection** 30 Vp-p
- **Input Impedance** 10 M Ω
- **Sampling Modes** Software, pacer or external
- **Input Range** (V, software programmable)

PCL-818L/818HD					
Bipolar	± 10	± 5	± 2.5	± 1.25	± 0.625
Unipolar*	N/A	0 ~ 10	0 ~ 5	0 ~ 2.5	0 ~ 1.25
Accuracy (% of FSR ± 1LSB)	0.1	0.1	0.2	0.2	0.4

* Note: PCL-818L doesn't support unipolar input range.

PCL-818HG								
Bipolar	± 10	± 5	± 1	± 0.5	± 0.1	± 0.05	± 0.01	± 0.005
Unipolar	N/A	0 ~ 10	N/A	0 ~ 1	N/A	0 ~ 0.1	N/A	0 ~ 0.01
Accuracy (% of FSR ± 1LSB)	0.1	0.1	0.2	0.2	0.4	0.4	0.8	0.8

Analog Output

- **Channels** 1
- **Resolution** 12 bits
- **Output Rate** Static update
- **Output Range** (V, software programmable)

Internal Reference	Unipolar	0 ~ 5, 0 ~ 10
External Reference		0 ~ 10, 0 ~ -10

Digital Input

- **Channels** 16
- **Compatibility** 5 V/TTL
- **Input Voltage** Logic 0: 0.8 V max.
Logic 1: 2.0 V min.

Digital Output

- **Channels** 16
- **Compatibility** 5 V/TTL
- **Output Voltage** Logic 0: 0.4 V max.
Logic 1: 2.4 V min.
- **Output Capability** Sink: 8 mA
Source: -0.4 mA

Timer/Counter

- **Channels** 1
- **A/D Pacer** 32-bit with 10 MHz or 1 MHz time base
- **Max. and Min. Rates** 2.5 MHz and 0.00023 Hz
- **Counter** One 16-bit counter with 100 kHz time base

General

- **Power Consumption** +5 V @ 210 mA typical, 500 mA max.
+12 V @ 20 mA typical, 100 mA max.
-12 V @ 20 mA typical, 40 mA max.
- **I/O Connector** 1 x DB37 female
2 x 20-pin box header
- **Dimensions (L x H)** 155 x 100 mm (6.1" x 3.9")
- **Operating Temperature** 0 ~ 50° C (32 ~ 122° F)
- **Storage Temperature** -20 ~ 65° C (-4 ~ 149° F)
- **Operating Humidity** 5 ~ 95% RH, non-condensing (refer to IEC 68-2-3)